

ABSTRACT OF THE DISCLOSURE

This specification discloses a symbol timing recovery circuit, which includes a transform value generation circuit, a selection circuit, a plurality of accumulators, and a comparison module. Without performing phase difference calculation, the invention is a symbol timing recovery circuit that can directly use digital in-phase signal components and digital quadrature signal components of consecutive two symbols to obtain an optimal sampling point. The invention also provides a phase demodulator with a simple structure.